IPMI Firmware / BIOS Release Notes Form

Product Name	**X11DPU**
Release Version	**2.1**
Release Date	**6/14/2018**
Previous Version	**2.0b**
Update Category	**Recommended**
Dependencies	**None**
Important Notes	**None**
Enhancements	**1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.** **2. Updated Purley RC 154.R13, SPS 4.0.04.340 and ACM 1.3.7, SINIT ACM 1.3.4.** **3. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v5.4.0.1039.** **4. Added support for UEFI mode PXE boot of F12 hot key Net boot.** **5. Added BIOS/ME downgrade check for SPS 4.0.4.340.** **6. Added one event log to record that the event log is full.** **7. Displayed PPR setup item.**
New features	**N/A**

Fixes	1. Fixed problem of SUM GetSataInfo showing incorrect "Configuration Type" when setting "Configure sSATA as" to "AHCI" or "RAID" on sSATA controller. 2. Fixed problem of Ctrl + Alt + Del causing system to hang after flashing BIOS. 3. Fixed problem of system hanging when installing Linux OS after COM1 & COM2 IO/IRQ exchange. 4. Fixed failure of WDT function.

2.0b (2/21/2018)

1. Updated CPU microcode SRV_B_216 for Skylake-SP H0/M0/U0 stepping CPUs.
2. Added setup item "SmcBusMasterEn" for enabling Parent bridge of the devices in the gSmcPciForceEnableBusMasterList.
3. Updated SPS 4.00.04.313 PLR version.
4. Updated Purley RC 151.R03.
5. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.
6. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel.
7. Fixed problem of DMI being cleared when running SUM LoadDefaultBiosCfg.
8. Disabled CPU2 IIO PCIe root port ACPI hot plug function.
9. Fixed issue with IPMI force boot.
10. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.
11. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.
12. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.