IPMI Firmware / BIOS Release Notes Form

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Product Name	X11DSC+
Release Version	2.1
Release Date	7/6/2018
Previous Version	2.0b
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated CPU microcode to address 'Spectre' derivatives (CVE-
	2018-3639 & CVE-2018-3640) security issue.
	2. Updated BIOS ACM 1.3.7 and SINIT ACM 1.3.4.
	3. Updated SPS 4.00.04.340 PL version.
	4. Updated 5.12_PurleyCrb_0ACFD088 for Purley Skylake platform
	PLR7, BKC 2018 WW20.
	5. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v5.4.0.1039.
	6. Added support for UEFI mode PXE boot of F12 hot key Net boot.
	7. Added BIOS/ME downgrade check for SPS 4.0.4.340.
	8. Added support for RFC3021.
	9. Corrected help message for TPH BIOS setup items.
	10. Displayed PPR setup item.
	11. Added support for SATA FLR with enabled as default.

	12. Added a patch to prevent reboot hang when installing AVAL APX-
	3224 card.
	13. Added one event log to record that the event log is full.
New features	N/A
	1. Fixed problem of SUM GetSataInfo showing incorrect
	"Configuration Type" when setting "Configure sSATA as" to "AHCI" or
	"RAID" on sSATA controller.
	2. Fixed problem of Ctrl + Alt + Del causing system to hang after
	flashing BIOS.
Fixes	3. Fixed inability of AFUWIN to keep VMD setting.
	4. Fixed failure of WDT function.
	5. Fixed inability of SMBIOS Type 40 to report SIOM card AOC-MTG-
	i2T/i4T information.
	6. Fixed problem of system repeatedly rebooting when SIOM card
	AOC-MTG-i4T is plugged in.

2.0b (2/26/2018)

- 1. Updated 5.12_PurleyCrb_0ACFD085Beta for Purley Skylake platform PLR 4, BKC WW02.
- 2. Updated SPS 4.00.04.294 PLR 3.1 PV version.
- 3. Updated DMI CTLE value.
- 4. Updated re-driver for AOC-SLG3-2E4R.
- 5. Added setup item "SmcBusMasterEn" for enabling Parent bridge of the devices in the gSmcPciForceEnableBusMasterList.
- 6. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.
- 7. Updated CPU microcode SRV_B_216 for Skylake-SP H0/M0/U0 stepping CPUs.
- 8. Updated BIOS version to 2.0b.
- 9. Updated BIOS ACM 1.3.5 and SINIT ACM 1.3.3.
- 10. Changed maximum speed in SMBIOS type 4 to 4500Mhz.
- 11. Added one event log to record that the event log is full.
- 12. Added support for VMD settings to be preserved after flashing.
- 13. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel.
- 14. Fixed inability of BIOS to boot into OS with Intel P3608 PCIe NVMe drive installed.
- 15. Fixed issue with IPMI force boot.
- 16. Fixed issue of all commands requesting to be persistent.
- 17. Fixed malfunction of "SMBIOS Preservation" Disabled.
- 18. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.
- 19. Fixed problem of the endpoint PCIe device having error bits in PCI Status or Device Status register.
- 20. Fixed inability to set memory policy.
- 21. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.
- 22. Fixed failure of BIOS ECO ATT test case 306.
- 23. Disabled CPU2 IIO PCIe root port ACPI hot plug function.
- 24. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.