## **Notes to Release Notes Authors**

- All fields <u>must</u> be completed; please indicate as "N/A" if not applicable.
- Release Notes <u>should</u> document functional changes, dependencies, bug fixes and added features.
- Release Notes <u>must not</u> mention the methods of implementation and/or internal processes.
- Release Notes <u>must not</u> mention of silicon errata and/or other external causations.
- Release Notes <u>must not</u> mention confidential/proprietary information and/or trade secrets <u>of Supermicro or any third party</u>.
- Please delete this page and the signature fields before submitting the document in <u>PDF</u>
   <u>Format</u> to Marketing for publication.

## **IPMI Firmware / BIOS Release Notes Form**

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Product Name	X10QRH+
Release Version	3.1
Release Date	7/5/2018
<b>Previous Version</b>	3.0a
<b>Update Category</b>	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol> <li>Updated CPU microcode for Intel security issue.</li> <li>Modified threshold limit of ECC memory to 100.</li> </ol>
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)		
3.0a (3/1/2018)		
1. Updated CPU microcode of Broadwell B0, M0, and R0 to version 2A and Hoversion 3C.	aswell CO, C1, and M1 to	
2. Updated CPU4 PCIE RX_CTLE_PEAK_GEN3_0 and RX_CTLE_PEAK_GEN3_1	per SI lab requirement.	
Product Manager	Date	