IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRG-O+-CPU
Release Version	3.1
Release Date	7/13/2018
Previous Version	3.0b
Update Category	Recommended
Dependencies	None
Important Notes	N/A
Enhancements	 Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue. Updated BIOS version to 3.1 for formal release.
New features	N/A
Fixes	1. Set Descriptor Region (Bit 0) of BIOS Region Write Access to "No".
	2. Fixed issue of SMBIOS System Slot (Type 9) Bus Numbers not
	matching.
	3. Fixed issue of PCIe being out of resources when LAN card and GPU
	cards are under one PLX bridge.

Release Notes from Previous Release(s)	
3.0b (4/23/2018)	
 Updated BIOS revision to 3.0b for formal release. Fixed failure of add-on card (AOC-S3108L-H8iR) to display OPROM message on X9DRG-O-PCIE PCIe extended board. 	
3.0a (2/27/2018)	
 Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue. Updated BIOS version to 3.0a for formal release. Updated for Intel security patch. Updated Onboard VGA (Aspeed) VBIOS to v1.02.07. Fixed problem of X10DRG-O+-CPU hanging at post code 33 after 544 cycles for system on/off. 	