

IPMI Firmware / BIOS Release Notes Form

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Product Name	X11DGQ
Release Version	2.1
Release Date	8/10/2018
Previous Version	2.0b
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue. 2. Changed BIOS revision to 2.1. 3. Enabled the Jumperless BIOS Flash feature. 4. Added support for AOC-SLG3-2M2 rev. 1.01 auto-bifurcation. 5. Added Pre-Go-S1 feature. 6. Added Intel VMD Technology page to BIOS Setup page. 7. Added support for NVIDIA P2P.
New features	N/A
Fixes	1. Set Descriptor Region of BIOS Region Write Access to "No". 2. Fixed malfunctioning of "Wait for F1 If Error" function. 3. Fixed failure to detect hot plugged NVMe in Legacy Mode if OS is

	up first without any NVMe disk installed.
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Release Notes from Previous Release(s)

2.0b (2/28/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Changed BIOS revision to 2.0b.
3. Removed BIOS time stamp from main BIOS setup page.
4. Added new FRU name for AOM-SXMV board.
5. Displayed the CPU T State Control Page on BIOS Setup Page.
6. Updated SMBus address for AOM-SXMV board after hardware rework.
7. Fixed inability to use OOB SUM to get current BIOS configuration file.
8. Fixed error when using AFUEFI tool to downgrade to previous ECO BIOS.
9. Fixed problem of system hanging at POST code "91" after BIOS update with EFI shell command "reset".

2.0 (11/29/2017)

1. Changed BIOS revision to 2.0.
2. Updated Intel Xeon microcode for Skylake-EP.
3. Updated Intel Server Platform Services 04.00.04.288.0 for Purley Platforms.
4. Updated Intel BIOS ACM Firmware to v1.3.4 and SINIT ACM Firmware to v1.3.2.
5. Updated RSTe VMD/SATA/sSATA driver to v5.3.0.1052.
6. Added support for 16GB x 4 density DRAM Chip (64GB in total for 2rank x 4 RDIMM).
7. Updated Setup item "Cores Enabled" in BIOS CPU Configuration setup page.
8. Updated SMBIOS revision from 3.0 to 3.1.
9. Removed the Unsupported Memory Frequency setting in BIOS page.
10. Fixed inability of DMI Customized Information to preserve after user implements BIOSLoadDefault or CMOS Clear Action.