IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X11SPH-nCT(P)F
Release Version	2.1
Release Date	8/29/2018
Previous Version	2.0b
Update Category	Critical
Dependencies	None
Important Notes	None
Enhancements	1. Updated CPU microcode to address 'Spectre' derivatives (CVE-
	2018-3639 & CVE-2018-3640) security issue.
	2. Changed BIOS version to 2.1.
	3. Updated 5.12_PurleyCrb_0ACFD088 for Purley Skylake platform
	PLR7, BKC 2018 WW20.
	4. Updated BIOS ACM 1.3.7 and SINIT ACM 1.3.4.
	5. Added one event log to record that the event log is full.
	6. Added support for VMD settings to be preserved after flashing.
	7. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v5.4.0.1039.
	8. Corrected BIOS/ME downgrade check for SPS 4.0.4.340.
	9. Added support for UEFI mode PXE boot of F12 hot key Net boot.
	10. Added setup item "SmcBusMasterEn" for enabling Parent bridge
	of the devices in the gSmcPciForceEnableBusMasterList.
	11. Added support for SATA FLR with enabled as default.

New features	N/A
Fixes	1. Fixed problem of DMI being cleared when running SUM
	UpdateBios.
	2. Fixed problem of Ctrl + Alt + Del causing system to hang after
	flashing BIOS.
	3. Rolled back SVN_3413 to fix failure of WDT function.
	4. Fixed problem of BIOS reporting incorrect SMBIOS Type 40 when
	device is installed on Slot 5 and no device is installed on Slot 6.
	5. Fixed issue with IPMI firmware capability.

2.0b (2/26/2018)

- 1. Updated CPU microcode to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
- 2. Changed BIOS revision to 2.0b.
- 3. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.
- 4. Updated 5.12_PurleyCrb_0ACFD085Beta for Purley Skylake platform PLR 4, BKC WW02.
- 5. Added setup item "SmcBusMasterEn" for enabling Parent bridge of the devices in the gSmcPciForceEnableBusMasterList.
- 6. Fixed failure of BIOS ECO ATT test case 306.
- 7. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.
- 8. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.
- 9. Disabled CPU2 IIO PCIe root port ACPI hot plug function.
- 10. Fixed issue with IPMI force boot.
- 11. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.