

# IPMI Firmware / BIOS Release Notes Form

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<b>Product Name</b>	<b>X11DPi-N(T)</b>
<b>Release Version</b>	<b>2.1</b>
<b>Release Date</b>	<b>6/29/2018</b>
<b>Previous Version</b>	<b>2.0b</b>
<b>Update Category</b>	<b>Recommended</b>
<b>Dependencies</b>	<b>None</b>
<b>Important Notes</b>	<b>None</b>
<b>Enhancements</b>	<b>1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 &amp; CVE-2018-3640) security issue. 2. Changed BIOS revision to 2.1. 3. Enabled the Jumperless BIOS Flash feature.</b>
<b>New features</b>	<b>N/A</b>
<b>Fixes</b>	<b>1. Fixed certain SUM-related issues on Project Board. 2. Set Descriptor Region of BIOS Region Write Access to "No".</b>

**Release Notes from Previous Release(s)**

**2.0b (2/28/2018)**

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Changed BIOS revision to 2.0b.
3. Added maximum performance option under Energy Performance on BIOS setup page.
4. Removed BIOS time stamp from main BIOS setup page.
5. Prevented certain corner cases that cause ME update to fail when using AFUEFI tool.
6. Fixed inability of certain SKX CPUs to display the microcode version correctly on BIOS setup page.
7. Fixed inability to use OOB SUM to get current BIOS configuration file.
8. Fixed problem of system hanging at POST code "91" after BIOS update with EFI shell command "reset".

*Product Name:* X11DPi-N(T)

*Release Version:* 2.0a

*Release Date:* 12/5/2017

*Previous Version:* 2.0

*Update Category:* Critical

*Dependencies:*

None

*Important Notes:*

N/A

*Enhancements:*

1. Changed BIOS revision to 2.0a.
2. Updated Intel Xeon microcode for Skylake-EP.

*New features:*

N/A

*Fixes:*

N/A

*Product Name:* X11DPi-N/X11DPi-NT

*Release Version:* 2.0

*Release Date:* 11/28/2017

*Previous Version:* 1.0b

*Update Category:* Critical

*Dependencies:*

None

*Important Notes:*

N/A

*Enhancements:*

1. Changed BIOS revision to 2.0.
2. Updated Intel Xeon microcode for Skylake-EP.
3. Updated Intel Server Platform Services 04.00.04.288.0 for Purley Platforms.
4. Updated Intel BIOS ACM v1.3.4/SINIT ACM v1.3.2.
5. Updated RSTe Component to revision 5.3.0.1052.
6. Updated Setup item "Cores Enabled" in BIOS CPU Configuration setup page.
7. Updated Intel x722 NVM to support Auto Detect PhyID for 1G SKU and 10G-BaseT SKU.
8. Removed the unsupported Memory Frequency setting in BIOS page.

*New features:*

N/A

*Fixes:*

1. Fixed inability of DMI Customized Information to preserve after user implements BIOSLoadDefault or CMOS Clear Action.

*Product Name: X11DPI-N(T)*

*Revision: 1.0b Previous Revision: 1.0a*

*Release Date: 10/12/2017*

*Update Category: Critical*

*Dependencies:*

*None*

*Important Notes:*

*None*

*Enhancements:*

1. *Changed BIOS revision to 1.0b.*
2. *Updated Intel Management Engine (ME) to Runtime EOM.*
3. *Added Supermicro Update Manager (SUM) Inband Support.*
4. *Updated Intel Server Platform Services 04.00.03.219.0 for Purley Platforms.*
5. *Updated Intel BIOS ACM v1.3.3/SINIT ACM v1.3.2.*
6. *Updated Intel Xeon Microcode for Skylake-EP to rev. 26 for Intel Xeon Skylake-EP H0/H0-QS.*
7. *Updated Intel Server Platform Services 04.00.03.235.0 for Purley Platforms.*
8. *Updated SMBIOS Revision from 3.0 to 3.1.*
9. *Updated WHQL USB Mapping.*
10. *Added Setup item "Core Disable Bitmap(Hex)" to BIOS CPU Configuration setup page.*
11. *Updated for Windows 7 Keyboard/Mouse Installation Support.*
12. *Implemented SDDC/ADDC event logging to IPMI.*

*New Features:*

1. *Set display of setup option for always turbo mode function as default.*

*Fixes:*

*N/A*

*Product Name: X11DPI-N/NT*

*Revision: 1.0a Previous Revision: 1.0*

*Release Date: 6/9/2017*

*Update Category: Critical*

*Dependencies:*

*None*

*Important Notes:*

*None*

*Enhancements:*

1. *Updated Intel Xeon microcode for Skylake-EP to rev. 22 for Intel Xeon Skylake-EP H0/H0-QS.*
2. *Updated Intel Server Platform Services to 04.00.03.206.0 for Purley Platforms.*
3. *Changed BIOS revision to 1.0a.*

*New Features:*

*N/A*

*Fixes:*

*N/A*

