

IPMI Firmware / BIOS Release Notes Form

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Product Name	X11DDW-L/NT
Release Version	2.1
Release Date	9/14/2018
Previous Version	2.0b
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.2. Changed BIOS version to 2.1.3. Updated 5.12_PurleyCrb_0ACFD088 for Purley Skylake platform PLR7, BKC 2018 WW20.4. Updated SPS 4.0.4.381 for INTEL-SA-00131 Security Advisory to address CVE-2018-3643 and CVE-2018-3644 security issues.5. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v5.4.0.1039.6. Corrected default setting for Enable SmcBusMasterEn setup item.7. Added BIOS/ME downgrade check for SPS 4.0.4.340.8. Added support for UEFI mode PXE boot of F12 hot key Net boot.9. Added one event log to record that the event log is full.10. Updated BIOS ACM 1.3.7 and SINIT ACM 1.3.4.11. Displayed PPR setup item.

	<p>12. Removed COM1 information from BIOS Setup.</p> <p>13. Enabled RSD support.</p>
New features	N/A
Fixes	<p>1. Fixed problem of SUM GetSataInfo showing incorrect "Configuration Type" when setting "Configure sSATA as" to "AHCI" or "RAID" on sSATA controller.</p> <p>2. Fixed problem of Ctrl + Alt + Del causing system to hang after flashing BIOS.</p> <p>3. Fixed missing NVDIMM ADR setup item.</p> <p>4. Fixed problem of system hanging when installing Linux OS after COM1 & COM2 IO/IRQ exchange.</p> <p>5. Fixed issue with IPMI firmware capability to remove multi asset information.</p>

Release Notes from Previous Release(s)

2.0b (3/7/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Changed BIOS revision to 2.0b.
3. Added setup item "SmcBusMasterEn" for enabling Parent bridge of the devices in the gSmcPciForceEnableBusMasterList.
4. Updated Purley RC 151.R03.
5. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.
6. Changed CECC Threshold to 100.
7. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel.
8. Set enabled JBOF support as default.
9. Disabled CPU2 IIO PCIe root port ACPI hot plug function.
10. Fixed issue with IPMI force boot.
11. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.
12. Fixed problem of DMI being cleared when running SUM LoadDefaultBiosCfg.
13. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.
14. Fixed problem of system hanging in POST when BIOS is updated in UEFI shell and soft-reset is performed.

2.0 (12/6/2017)

1. Changed BIOS revision to 2.0a.
2. Updated CPU microcode for Skylake-EP H0/B0 stepping CPUs.