BIOS Release Notes Form

Product Name	**B11DPE**
Release Version	**3.0a**
Release Date	**9/14/2018**
Previous Version	**2.0c**
Update Category	**Recommended**
Dependencies	**None**
Important Notes	**None**
Enhancements	**1. Updated Intel BKC Version 2018_WW52 (KIT #130606).** **2. Updated for NVDIMM ADR values for 1 & 2 CPUs.** **3. Updated for PBF feature.** **4. Updated the type 9/type 40 reporting.** **5. Updated for Mezzanine 25G card sensor reading.**
New features	**N/A**
Fixes	**1. Fixed issue with CPU Throttling Event Logging & Alert LED Function.** **2. Fixed failure of SUM cases TC210 and TC304.**

Release Notes from Previous Release(s)

2.0b (2/21/2018)

1. Updated Intel microcode to 0x43 to address Intel side-channel security problem.
2. Changed revision to 2.0b.
3. Updated Terminal setup COM2 string for SUM.

2.0a (12/7/2017)

1. Updated in-band module to support latest SUM.
2. Changed SMM buffer size to 256K for SUM.
3. Updated ME firmware to 288 to fix Intel security issue.
4. Initialized default values for boot order.
5. Updated CPU Microcode to 3A to solve security issue.
6. Changed revision to 2.0a.
7. Fixed problem of DMI data being erased during SUM testing.