

IPMI Firmware / BIOS Release Notes Form

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Product Name	X11DPX-T
Release Version	3.0b
Release Date	3/4/2019
Previous Version	2.1
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Changed BIOS version to 3.0b.2. Added support for Purley Refresh platform.3. Updated CPU microcode MB750654_0200005A for Skylake-SP H0/M0/U0 CPUs.4. Updated SATA RAID OPR0M/EFI driver to RSTe PreOS v6.0.0.1024.5. Added support for Monitor Mwait feature.6. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.7. Set PCIe correctable and uncorrectable (non-fatal) error report to disabled by default.8. Disabled unused SPS ME "CUPS IIO" and "Volumetric Airflow" sensor.9. Added support for Linux built-in utility efibootmgr.10. Updated valid range of IPMI setup item VLAN ID to 1-4094.

	<p>11. Set NVDIMM ADR timeout to 600us.</p> <p>12. Enhanced the solution for the error/warning message from dmidecode after a modification by AMIDE.</p> <p>13. Updated to SPS 4.1.02.174 or above for INTEL-SA-00185 Security Advisory and RC 549.D13 or above for INTEL-SA-00192 Security Advisory.</p>
New features	N/A
Fixes	<p>1. Fixed malfunction of CPU PBF (Prioritized Base Frequency).</p> <p>2. Fixed incorrect PBF high frequency core number when Hyper-Threading is disabled.</p> <p>3. Modified UEFI network description to IPv4/IPv6 to follow Industry Standard.</p> <p>4. Patched problem of system hanging at 0x94 when plugging in Nvidia Tesla T4 card.</p>

Release Notes from Previous Release(s)

2.1 (7/31/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS revision to 2.1.
3. Added new setup items "SLOT1 x8 & SLOT2 x8" and "SLOT5 x8 & SLOT6 x8" in IOU0.
4. Updated Purley RC 154.R13, SPS 4.0.04.340 and ACM 1.3.7, SINIT ACM 1.3.4.
5. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v5.4.0.1039.
6. Added support for UEFI mode PXE boot of F12 hot key Net boot.
7. Added BIOS/ME downgrade check for SPS 4.0.4.340 and later versions.
8. Added one event log to record that the event log is full.
9. Corrected help message for TPH BIOS setup items.
10. Displayed PPR setup item.
11. Fixed problem of SUM GetSataInfo showing incorrect "Configuration Type" when setting "Configure sSATA as" to "AHCI" or "RAID" on sSATA controller.
12. Fixed problem of Ctrl + Alt + Del causing system to hang after flashing BIOS.
13. Fixed problem of system hanging when installing Linux OS after COM1 & COM2 IO/IRQ exchange.
14. Fixed failure of WDT function.
15. Added workaround for low GPU P2P bandwidth.
16. Changed Slot Data Bus Width for slot 2 and slot 6.

2.0c (3/13/2018)

1. Updated CPU microcode SRV_P_229 for Skylake-SP H0/M0/U0 stepping CPUs to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
2. Changed BIOS revision to 2.0c.
3. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.
4. Added support for VMD settings to be preserved after flashing, with disabled as default.
5. Changed maximum speed in SMBIOS type 4 to 4500Mhz.
6. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel.
7. Fixed problem of DMI being cleared when running SUM LoadDefaultBiosCfg.
8. Disabled CPU2 IIO PCIe root port ACPI hot plug function.
9. Fixed issue with IPMI force boot.
10. Fixed inability to set memory policy.
11. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.
12. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.
13. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.
14. Patched a workaround for some motherboards' on board LAN having correctable error or GEN speed drop.

2.0b (1/31/2018)

1. Updated BIOS version to 2.0b and changed BIOS size to 32MB.
2. Rolled back CPU microcode to MB750654_02000035 for Skylake-EP H0/M0/U0 stepping CPUs.
3. Fixed failure of BIOS ECO ATT test case 306.
4. Fixed problem of PCIe slot 7 strings name in BIOS setup not matching motherboard's silkscreen.
5. Fixed failure to check IOBP settings.

