## **IPMI Firmware / BIOS Release Notes Form**

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Product Name	X11DPU-Z+
Release Version	3.0a "SPS : 4.1.03.239"
Release Date	1/12/2019
Previous Version	2.0b
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Added support for Purley Refresh platform.
	2. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v6.0.0.1024.
	3. Set BMC MAC address to UUID for system without onboard LAN1 or
	with onboard LAN1 disabled.
	4. Set PCIe correctable and uncorrectable (non-fatal) error report to
	disabled by default.
	5. Set RFC4122 encoding to only be enabled for build time produced by
	IPMI 1.29 or newer.
	6. Updated CPU microcode SRV_P_264 for Skylake-SP H0/M0/U0 CPUs.
	7. Disabled unused SPS ME "CUPS IIO" and "Volumetric Airflow" sensor.
	8. Updated valid range of IPMI setup item VLAN ID to 1-4094.
New features	N/A

Fixes	1. Fixed problem of setup item "Quiet Boot" staying enabled after
	flashing BIOS with "Quiet Boot" disabled.

## Release Notes from Previous Release(s)

## 2.0b (2/27/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.

2. Updated 5.12\_PurleyCrb\_0ACFD085Beta for Purley Skylake platform PLR 4, BKC WW02.

3. Added setup item "SmcBusMasterEn" for enabling Parent bridge of the devices in the gSmcPciForceEnableBusMasterList.

4. Added new IPMI Standard Event feature.

5. Changed CECC Threshold to 100.

6. Fixed failure of BIOS ECO ATT test case 306.

## 2.0 (11/30/2017)

1. Updated 5.12\_PurleyCrb\_0ACFD084\_BETA for Purley Skylake platform PLR 3.1.

2. Updated CPU microcode SRV\_P\_214 for Skylake-EP H0/M0/U0 stepping CPUs.

3. Updated SPS 4.00.04.294 PLR 3.1 PV version.

4. Updated BIOS ACM 1.3.4.

5. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v5.3.0.1052.

6. Added support for 16GB x 4 density DRAM Chip (64GB in total for 2rank x 4 RDIMM).

7. Enabled CPU core by Core Count.

8. Showed number of active UPI links.

9. Removed PCI PERR/SERR Support item and enabled reporting PCIe error by default.

10. Implemented SDDC/ADDDC event logging to IPMI.

11. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.

12. Set flashing BIOS/ME to be skipped when trying to downgrade the SPS ME firmware from 4.0.4.288 to previous version.

13. Fixed problem of system hanging at LSI OPROM with two LSI 3108 cards installed.

14. Fixed problem of TPM 1.2 PS index not being Write-Protected so that the content of TPM 1.2 PS index still can be modified after TPM 1.2 is nvLocked.

15. Fixed problem of serial console output showing SMC logo when EarlyVideo logo item is disabled.