# **IPMI Firmware / BIOS Release Notes Form**

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Product Name	X11DPi-N(T)
Release Version	3.0c SPS: 04.01.04.256.0
Release Date	4/1/2019
Previous Version	3.0a
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Changed BIOS revision to 3.0c.
	2. Updated Intel Reference Code to BKC 2019_WW12 Rev: RC0580.D04.
	3. Updated Intel Xeon microcode for Skylake-EP to Rev. 5A for Intel
	Xeon Skylake-EP H0/H0-QS.
	4. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 21 for
	Intel Xeon Scalable Cascade Lake Server B0-QS.
	5. Added Cascade Lake Server B1 Stepping CPU Support with Rev. 21 for
	Intel Xeon Scalable Cascade Lake Server B1-QS.
	6. Updated Intel Server Platform Services 04.01.04.256.0 for Purley-
	Refresh Platforms.
	7. Updated Intel BIOS ACM firmware to v1.7.1 and SINIT ACM firmware
	to v1.7.2.
	8. Updated SMBIOS Types 16 and 17 for DCPMM support.
	9. Set SDDC+1/ADDDC to enabled by default.

New features	N/A
Fixes	1. Fixed problem of SKX/CLX 4xxx SKU CPU causing memory training
	error with Micron RDIMM (18ASF2G72PDZ-2G6E1).
	2. Fixed malfunction of hotkey F12 when switching onboard LAN Option
	ROM to EFI.

#### Release Notes from Previous Release(s)

#### 3.0a (1/11/2019)

- 1. Added support for Purley Refresh platform.
- 2. Changed BIOS revision to 3.0a.
- 3. Updated Intel Reference Code to BKC WW52 Rev: RC0566.D03.
- 4. Updated Intel Xeon microcode for Skylake-EP to Rev. 57 for Intel Xeon Skylake-EP H0/H0-QS.
- 5. Updated Intel BIOS ACM firmware to v1.7.0 and SINIT ACM firmware to v1.7.1.
- 6. Updated RSTe VMD/SATA/sSATA driver to v6.0.0.1024.
- 7. Enabled RFC4122 UUID support.
- 8. Updated BIOS Setup Template to v0.7.
- 9. Fixed malfunction of Intel SST-BF feature.

## 2.1 (6/29/2018)

- 1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
- 2. Changed BIOS revision to 2.1.
- 3. Enabled the Jumperless BIOS Flash feature.
- 4. Fixed certain SUM-related issues on Project Board.
- 5. Set Descriptor Region of BIOS Region Write Access to "No".

### 2.0b (2/28/2018)

- 1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
- 2. Changed BIOS revision to 2.0b.
- 3. Added maximum performance option under Energy Performance on BIOS setup page.
- 4. Removed BIOS time stamp from main BIOS setup page.
- 5. Prevented certain corner cases that cause ME update to fail when using AFUEFI tool.
- 6. Fixed inability of certain SKX CPUs to display the microcode version correctly on BIOS setup page.
- 7. Fixed inability to use OOB SUM to get current BIOS configuration file.
- 8. Fixed problem of system hanging at POST code "91" after BIOS update with EFI shell command "reset".