IPMI Firmware / BIOS Release Notes Form

Product Name	**X11DGQ**
Release Version	**3.0c SPS: 04.01.04.256.0**
Release Date	**3/28/2019**
Previous Version	**3.0a**
Update Category	**Recommended**
Dependencies	**None**
Important Notes	**None**
Enhancements	**1. Changed BIOS revision to 3.0c.** **2. Updated Intel Reference Code to BKC 2019_WW12 Rev: RC0580.D04.** **3. Updated Intel Xeon microcode for Skylake-EP to Rev. 5A for Intel Xeon Skylake-EP H0/H0-QS.** **4. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 21 for Intel Xeon Scalable Cascade Lake Server B0-QS.** **5. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 21 for Intel Xeon Scalable Cascade Lake Server B1-QS.** **6. Updated Intel Server Platform Services 04.01.04.256.0 for Purley-Refresh Platforms.** **7. Updated Intel BIOS ACM firmware to v1.7.1 and SINIT ACM firmware to v1.7.2.** **8. Changed BIOS setup item SR-IOV to disabled as default.** **9. Added MAC address to the end of each network device boot option**

	to eliminate duplication of the network boot option name when onboard LAN OPROM is set to EFI.
New features	N/A
Fixes	1. Fixed malfunction of the temperature sensor of riser AOC-SLG3-2M2 on some slots. 2. Fixed problem of memory training error occurring on SKX/CLX 4xxx CPU SKU with Micron (18ASF2G72PDZ-2G6E1) RDIMM. 3. Fixed malfunction of hotkey F12 when switching onboard LAN Option ROM to EFI.

3.0a (1/14/2019)

1. Added support for Purley Refresh platform.
2. Changed BIOS revision to 3.0a.
3. Updated Intel Reference Code to BKC WW52 Rev: RC0566.D03.
4. Updated Intel Xeon microcode for Skylake-EP to Rev. 57 for Intel Xeon Skylake-EP H0/H0-QS.
5. Updated Intel BIOS ACM firmware to v1.7.0 and SINIT ACM firmware to v1.7.1.
6. Enabled RFC4122 UUID support.
7. Added support for Tesla T4 GPU.

2.1 (8/10/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS revision to 2.1.
3. Enabled the Jumperless BIOS Flash feature.
4. Added support for AOC-SLG3-2M2 rev. 1.01 auto-bifurcation.
5. Added Pre-Go-S1 feature.
6. Added Intel VMD Technology page to BIOS Setup page.
7. Added support for NVIDIA P2P.
8. Set Descriptor Region of BIOS Region Write Access to "No".
9. Fixed malfunctioning of "Wait for F1 If Error" function.
10. Fixed failure to detect hot plugged NVMe in Legacy Mode if OS is up first without any NVMe disk installed.

2.0b (2/28/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Changed BIOS revision to 2.0b.
3. Removed BIOS time stamp from main BIOS setup page.
4. Added new FRU name for AOM-SXMV board.
5. Displayed the CPU T State Control Page on BIOS Setup Page.
6. Updated SMBus address for AOM-SXMV board after hardware rework.
7. Fixed inability to use OOB SUM to get current BIOS configuration file.
8. Fixed error when using AFUEFI tool to downgrade to previous ECO BIOS.
9. Fixed problem of system hanging at POST code "91" after BIOS update with EFI shell command "reset".

2.0 (11/29/2017)

1. Changed BIOS revision to 2.0.
2. Updated Intel Xeon microcode for Skylake-EP.
3. Updated Intel Server Platform Services 04.00.04.288.0 for Purley Platforms.
4. Updated Intel BIOS ACM Firmware to v1.3.4 and SINIT ACM Firmware to v1.3.2.
5. Updated RSTe VMD/SATA/sSATA driver to v5.3.0.1052.
6. Added support for 16GB x 4 density DRAM Chip (64GB in total for 2rank x 4 RDIMM).
7. Updated Setup item "Cores Enabled" in BIOS CPU Configuration setup page.
8. Updated SMBIOS revision from 3.0 to 3.1.
9. Removed the Unsupported Memory Frequency setting in BIOS page.
10. Fixed inability of DMI Customized Information to preserve after user implements BIOSLoadDefault or CMOS Clear Action.