BIOS Release Notes Form

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Product Name	AOM-X110PI-LBG-P/MBD-X110PI-CPU-P
Release Version	3.1
Release Date	5/10/2019
Previous Version	3.0a
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated Intel Reference Code to BKC WW12 Rev: RC0584.D01.
	2. Updated Intel Xeon microcode for Skylake-EP to Rev. 5E for Intel Xeon Skylake-EP H0/H0-QS.
	3. Added Cascade Lake Server A0 Stepping CPU Support with Rev. 10 for Intel Xeon Scalable Cascade
	Lake Server A0-QS.
	4. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade Lake
	Server B0-QS.
	5. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade Lake
	Server B1-QS.
	6. Updated Intel Server Platform Services 04.01.04.296.0 for Purley-Refresh Platforms.
	7. Updated Intel BIOS ACM firmware to v1.7.1 and SINIT ACM firmware to v1.7.2.
	8. Updated for new VRM.
	9. Updated RSTe VMD/SATA/sSATA driver to v6.1.0.1017.
	10. Updated for help strings.
	11. Set RAS feature SDDC+1 to disabled by default, RAS feature ADDDC to enabled by default, and Leaky
	Bucket to 2.15 minutes with DDR speed 2666MHz.
	12. Changed Memory Correctable Threshold to 512.
New features	N/A
Fixes	N/A