## **IPMI Firmware / BIOS Release Notes Form**

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Product Name	X10DRC(-I-T4+/I-LN4+)
Release Version	3.1c SPS: 3.1.3.72
Release Date	04/27/2019
Previous Version	3.1
<b>Update Category</b>	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated Intel Server Platform Services 3.1.3.72 for Grantley
	Refresh Platforms.
	2. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS
	v6.1.0.1017.
	3. Updated VBIOS and VGA EFI Driver to 1.09 to fix ASpeed CVE-2019-
	6260 security issue.
	4. Updated Haswell-EP/Broadwell-EP CPU microcode from
	SRV_P_273.
	5. Updated valid range of IPMI setup item VLAN ID to 1-4094.
	6. Added support for RFC4122 UUID format feature to set RFC4122
	encoding from build time produced by IPMICFG 1.29 tool or newer.
	7. Forced a global reset if SPI descriptor is not write-protected after
	BIOS flash.
	8. Implemented anti-rollback for FDT Read-Only.

	9. Implemented multi-line IPMI page text and string.
	10. Added support for setting IPv6 Static Router1 prefix length and
	value in BIOS setup menu feature.
New features	N/A
Fixes	Displayed the driver health support pages to support LSI
	(Broadcom 9440-8i) driver health status.
	2. Fixed malfunction of Windows Consistent Device Naming (CDN).
	3. Fixed problem of BIOS always retrying boot when Re-Try Boot is
	Disabled.
	4. Fixed malfunction of METW if many error events are triggered
	within a very short time.

## 3.1 (6/8/2018)

- 1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
- 2. Changed BIOS revision to 3.1.
- 3. Added support for IPMI IPV6.
- 4. Removed unsupported memory frequency options from setup menu.
- 5. Updated Broadwell-EP RC 4.4.0 release.
- 6. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC).
- 7. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".
- 8. Fixed failure of SMBIOS to change to default even if preserve SMBIOS setup item is set to disabled during recovery.

## 3.0a (2/9/2018)

- 1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
- 2. Changed BIOS revision to 3.0a.
- 3. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
- 4. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v4.7.0.1014 (RSTe SATA 4.7.0.1069 and NVMe 4.7.0.2063).
- 5. Removed support for using Ctrl+home to trigger recovery.
- 6. Added support for UEFI mode PXE boot via F12 hot key Net boot.
- 7. Fixed inability of SUM utility to get "Setup Prompt Timeout" setup item.
- 8. Updated Broadwell-EP RC 4.3.0 PLR11 release.
- 9. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
- 10. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
- 11. Added tCCD\_L Relaxation item under Memory Configuration menu.
- 12. Added SumBbsSupportFlag into DAT file.
- 13. Added Intel SPI vulnerability patch for Grantley.
- 14. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.
- 15. Enhanced SMC Recovery Flash Boot Block feature.
- 16. Added support for JEDEC NVDIMM.
- 17. Implemented SMC Recovery Flash Boot Block feature.
- 18. Updated FlashDriver module to Label 5 in order to fix inability of system to enter recovery mode when MAIN block is updated 45% and then system powers off.
- 19. Added Ramaxel JEDEC ID to support Ramaxel memory.
- 20. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.
- 21. Corrected POST diagnostic signOn string.
- 22. Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.
- 23. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.
- 24. Fixed problem of MWAIT being disabled on BSP only.
- 25. Fixed inability to get correct Memory CECC DIMM location via SD5.