IPMI Firmware / BIOS Release Notes Form

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Product Name	X11DGO-T
Release Version	3.1 SPS: 4.1.04.296
Release Date	5/9/2019
Previous Version	3.0c
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	 Updated Skylake H0/M0/U0 CPU μ-code to version 5E and Cascade Lake B0/B1 to version 24. Updated Intel[®] RSTe package to version 6.1.0.1017. Updated RC0584.D01. Set SDDC Plus One or SDDC to disabled by default.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.0c (4/5/2019)

- 1. Updated Skylake H0/M0/U0 CPU μ -code to version 5A and Cascade Lake B0/B1 to version 21.
- 2. Updated Intel[®] RSTe package to ver. 6.0.0.1024.
- 3. Updated RC0578.D07.
- 4. Updated SPS 4.01.04.256.0.
- 5. Updated ADR time of 600 microcodes for PCU-assisted NVDIMM ADR flow.
- 6. Fixed inability to update BIOS by AFUEFI or AFULNX.

3.0a (1/16/2019)

- 1. Added support for Purley Refresh platform.
- 2. Updated Intel[®] RSTe package to ver. 6.0.0.1024.
- 3. Updated ADR time of 200 microcodes for PCU-assisted NVDIMM ADR flow.

2.1 (9/28/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.

- 2. Added support for MSMI feature.
- 3. Patched to support NVIDIA P2P transaction without performance penalty.
- 4. Updated Intel[®] RSTe 5.4.0 1039.
- 5. Added 15-second delay before getting PSU information from FRU.
- 6. Fixed inability to flash back to previous version of BIOS.