

IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X11DPU-XLL
Release Version	3.1 SPS: 4.1.4.296
Release Date	6/3/2019
Previous Version	2.0c
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	1. Updated Skylake-SP/Cascade Lake-SP CPU microcode. 2. Updated Intel BKCWW16 2019 PV PLR1. 3. Updated SPS_E5_04.01.04.296.0 from BKC WW16 2019. 4. Set ADDDC Sparing to enabled by default.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

2.0c (4/12/2018)

1. *Changed selections for enabled and disabled Hyper-Speed.*

2.0b (3/14/2018)

1. *Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security Patch issue.*
2. *Set OC to 2.38% on 6154 and 6146 CPUs for better yield rate.*
3. *Changed BIOS revision to 2.0b.*
4. *Updated 5.12_PurleyCrb_0ACFD084 for Purley Skylake platform PLR 3.1.*
5. *Updated SPS 4.00.04.294 PLR 3.1 PV version.*
6. *Fixed failure of BIOS ECO ATT test case 306.*
7. *Fixed problem of DMI being cleared when SUM LoadDefaultBiosCfg is run.*
8. *Fixed issue of system continuously rebooting when TPM module is plugged in.*

2.0a (1/6/2018)

1. *Set OC to 2.63% on 6154 and 6146 CPUs for better yield rate.*
2. *Updated 5.12_PurleyCrb_0ACFD084 for Purley Skylake platform PLR 3.1.*
3. *Updated SPS 4.00.04.294 PLR 3.1 PV version.*
4. *Updated CPU microcode SRV_P_217 for Skylake-EP H0/M0/U0 stepping CPUs.*
5. *Fixed problem of DMI being cleared when SUM LoadDefaultBiosCfg is run.*
6. *Fixed issue of system continuously rebooting when TPM module is plugged in.*

2.0 (11/28/2017)

1. *Updated 5.12_PurleyCrb_0ACFD083_BETA for Purley Skylake Platform BKC WW39.*
2. *Updated SPS 4.00.04.288 PLR version for security vulnerabilities.*
3. *Updated CPU microcode SRV_P_211 for Skylake-EP H0 stepping CPUs.*
4. *Hid SDDC Plus One and ADDDC Sparing option for unsupported project.*
5. *Fixed inability to enter setup menu when pressing "DEL" key if there is no boot device.*