

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DRL-i
Release Version	3.1c
Release Date	4/27/2019
Previous Version	3.1
Update Category	Critical
Dependencies	N/A
Important Notes	<p>ECO #22337 BIOS Image X10DRL9.427 Please update BIOS with attached Flash Utility in package.</p>
Enhancements	<ol style="list-style-type: none">1. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.2. Updated SATA/sSATA RAID OPROM/EFI driver to RSTe PreOS v5.5.0.1028 (RSTe 5.5.0.2015).3. Updated VBIOS and VGA EFI Driver to 1.09.4. Updated Haswell-EP/Broadwell-EP CPU microcode from SRV_P_268.5. Updated valid range of IPMI setup item VLAN ID to 1-4094.6. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.7. Forced a global reset if SPI descriptor is not write-protected after BIOS flash.8. Implemented anti-rollback for FDT Read-Only.9. Implemented multi-line IPMI page text and string.10. Updated EIP393007 & EIP411789 for TPM vulnerability when resuming S3.11. Added support for setting IPv6 Static Router1 prefix length and value in BIOS setup menu feature.12. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.1.0.1017.

New features	N/A
Fixes	<ol style="list-style-type: none"> 1. Displayed the driver health support pages to support LSI (Broadcom 9440-8i) driver health status. 2. Fixed malfunction of Windows Consistent Device Naming (CDN). 3. Fixed problem of BIOS always retrying boot when Re-Try Boot is Disabled. 4. Fixed malfunction of METW if many error events are triggered within a very short time.

3.1 (6/7/2018)

1. *Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.*
2. *Changed BIOS version to 3.1.*
3. *Added IPMI IPV6 support.*
4. *Removed unsupported memory frequency options from setup menu.*
5. *Updated Broadwell-EP RC 4.4.0 release*
6. *Fixed Afu /O command will clear SMC SMBIOS region (\$SMC).*
7. *Fixed SUM OOB GetSataInfo always shows "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".*
8. *Fixed based on Intel recommended settings to set Descriptor Region of BIOS Region Write Access to "No".*

3.0a (2/9/2018)

1. *Updated CPU microcode to address CVE-2017-5715 security issue.*
2. *Changed BIOS revision to 3.0a.*
3. *Updated Broadwell-EP RC 4.3.0 PLR11 release.*
4. *Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.*
5. *Added tCCD_L Relaxation item under Memory Configuration menu.*
6. *Added SumBbsSupportFlag into DAT file.*
7. *Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.*
8. *Added support for JEDEC NVDIMM.*
9. *Implemented SMC Recovery Flash Boot Block feature.*
10. *Added Ramaxel JEDEC ID to support Ramaxel memory.*
11. *Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.*
12. *Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.*
13. *Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.*
14. *Fixed problem of MWAIT being disabled on BSP only*