

IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X10DRL-LN4
Release Version	3.1c
Release Date	5/2/2019
Previous Version	3.1
Update Category	Critical
Dependencies	N/A
Important Notes	ECO #22328 BIOS Image X10DRLN9.502 Please update BIOS with attached Flash Utility in package.
Enhancements	<ol style="list-style-type: none">1. Changed BIOS revision to 3.1c.2. Forced a global reset if SPI descriptor is not write protected after BIOS flash.3. Implemented anti-rollback for FDT Read-Only.4. Implemented multi-line IPMI page text and string.5. Updated EIP393007 & EIP411789 for TPM vulnerability when resuming S3.6. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.7. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.1.0.1017.8. Updated VBIOS and VGA EFI Driver to 1.09.9. Updated Haswell-EP/Broadwell-EP CPU microcode from SRV_P_273 for INTEL-SA-00233 Security Advisory.10. Updated valid range of IPMI setup item VLAN ID to 1-4094.11. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.12. Added support for setting IPv6 Static Router1 prefix length and value in BIOS setup menu feature.

New features	N/A
Fixes	<ol style="list-style-type: none">1. Fixed problem of BIOS always retrying boot when Re-Try Boot is Disabled.2. Fixed malfunction of METW if many error events are triggered within a very short time.3. Displayed the driver health support pages to support LSI (Broadcom 9440-8i) driver health status.4. Fixed malfunction of Windows Consistent Device Naming (CDN).

BIOS 3.1 (6/7/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS revision to 3.1.
3. Updated Broadwell-EP RC 4.4.0 release.
4. Added support for IPMI IPV6.
5. Removed unsupported memory frequency options from setup menu.
6. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC).
7. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".
8. Fixed failure of SMBIOS to change to default even if preserve SMBIOS setup item is set to disabled during recovery.
9. Set Descriptor Region of BIOS Region Write Access to "No".

BIOS 3.0a (2/8/2018)

1. Updated CPU microcode to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
2. Changed BIOS revision to 3.0a.
3. Corrected POST diagnostic signOn string.
4. Patched for Micron Z11C DIMM to improve the stability.
5. Changed tCCD_L Relaxation default to 1 and changed string from Enabled to Auto.
6. Appended VPD - VB tag in VPD-W Tag(0x91) area.
7. Updated new MRC error log definition.
8. Updated Intel Server Platform Services 3.1.3.38 PLR7 for Grantley Refresh Platforms.
9. Added Intel SPI vulnerability patch for Grantley.
10. Updated Broadwell-EP B0/M0/R0 CPU uCode MEF406F1_0B000022.
11. Added "SMBIOS Preservation" Enabled/Disabled item for flash recovery.
12. Enhanced SMC Recovery Flash Boot Block feature.
13. Updated SATA RAID OPRM/EFI driver to RSTe PreOS v4.7.0.1014.
14. Updated Intel Server Platform Services 3.1.3.50 for Grantley Refresh Platforms.
15. Removed support for using Ctrl+home to trigger recovery.
16. Updated Broadwell-EP RC 4.3.0 PLR10 release.
17. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU E5-26xx SKU.
18. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability.
19. Added BBS reset function to SmcOobLoadBiosDefault().
20. Added SumBbsSupportFlag into DAT file.
21. Added support for JEDEC NVDIMM.
22. Added LSI 3008/3016 HBA sensor support.
23. Implemented SMC Recovery Flash Boot Block feature.
24. Fixed issue of SUM TC306 and TC317 failing in certain configuration cases.
25. Fixed problem of system hanging after Watch Dog function is enabled and then BIOS is updated.
26. Fixed missing Manufacturer string in SMBIOS type 17 when Ramaxel DIMM is plugged in.
27. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without

NVDIMM being installed.

- 28. Fixed problem of system hanging on POST code 0xB2.*
- 29. Fixed problem of system resetting or hanging after Watch Dog function is enabled and then BIOS is updated.*
- 30. Fixed inability to get correct Memory CECC DIMM location via SD5.*
- 31. Fixed inability to log Patrol Scrub UCE.*
- 32. Fixed problem of system hanging at 79 with some NVDIMMs.*
- 33. Fixed problem of incorrect PCI slot number showing on Legacy/UEFI mode.*