

IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X11DPG-OT-CPU
Release Version	3.1 SPS: 4.1.04.296
Release Date	04/30/2019
Previous Version	3.0c
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Changed BIOS revision to 3.1.2. Updated Intel Reference Code to BKC WW16 Rev: RC0584.D01.3. Updated Intel Xeon microcode for Skylake-EP to Rev. 5E for Intel Xeon Skylake-EP H0/H0-QS.4. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade Lake Server B0-QS.5. Added Cascade Lake Server B1 Stepping CPU Support with Rev. 24 for Intel Xeon Scalable Cascade Lake Server B1-QS.6. Updated Intel Server Platform Services 04.01.04.296.0 for Purley-Refresh Platforms.7. Updated RSTe VMD/SATA/sSATA driver to v6.1.0.1017.8. Set RAS Feature SDDC+1 to disabled by default.
New features	N/A
Fixes	N/A

Release Notes from Previous Release(s)

3.0c (4/9/2019)

1. Changed BIOS revision to 3.0c.
2. Updated Intel Reference Code to BKC 2019_WW12 Rev: RC0580.D04.
3. Set SDDC+1/ADDDC to enabled by default.
4. Temporarily fixed error for SKX (CLX) 4114 CPU memory training with Micron (18ASF2G72PDZ-2G6E1) RDIMM.

3.0b SPS: 04.01.04.256.0 (3/13/2019)

1. Changed BIOS revision to 3.0b.
2. Updated Intel Reference Code to BKC 2019_WW10 Rev: RC0578.D07.
3. Updated Intel Xeon microcode for Skylake-EP to Rev. 5a for Intel Xeon Skylake-EP H0/H0-QS.
4. Added Cascade Lake Server A0 Stepping CPU Support with Rev. 10 for Intel Xeon Scalable Cascade Lake Server A0-QS.
5. Added Cascade Lake Server B0 Stepping CPU Support with Rev. 21 for Intel Xeon Scalable Cascade Lake Server B0-QS.
6. Added Cascade Lake Server B1 Stepping CPU Support with Rev. 21 for Intel Xeon Scalable Cascade Lake Server B1-QS.
7. Updated Intel Server Platform Services 04.01.04.256.0 for Purley-Refresh Platforms.
8. Updated Intel BIOS ACM firmware to v1.7.0 and SINIT ACM firmware to v1.7.1.
9. Fixed inability to update BIOS by AFU tool.

3.0a (2/26/2019)

1. Added support for Purley Refresh platform.
2. Changed BIOS revision to 3.0a.
3. Updated Intel Reference Code to BKC 2019_WW06 Rev: RC0575.D01.
4. Updated Intel Xeon microcode for Skylake-EP to Rev. 5a for Intel Xeon Skylake-EP H0/H0-QS.
5. Updated Intel Server Platform Services 04.01.04.251.0 for Purley-Refresh Platforms.
6. Updated Intel BIOS ACM firmware to v1.7.0 and SINIT ACM firmware to v1.7.1.
7. Increased ADR timeout to 600us for NVDIMM.
8. Updated redriver setting on X10 PCIe board slots 6 & 7 according to hardware requirement.

2.1a (10/02/2018)

1. Changed BIOS revision to 2.1a.
2. Added support for the X11DPG-O-PCIe board.
3. Changed MMIO High Granularity size from 256G to 1024G to fix problem of hang 91 occurring with 8x V100 GPU.

2.1 (9/17/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS revision to 2.1.
3. Added code to program CPLD for backplane detection.
4. Updated Intel code to correct issue with NVIDIA P2P bandwidth.

5. Fixed inability to execute UEFI PXE boot with AOC-MGP-i2 and added i350 UEFI Option ROM for X11DPG-SN.
6. Fixed failures of Test Case 220 and 308 Test.

2.0b (2/26/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security patch issue.
2. Updated BIOS version to 2.0b.
3. Added support for IPMI to fix RSC-G2F-A66-X1 sensor reading.
4. Added 2U PLX riser card to support list.
5. Fixed failure of SIOM sensor reading.
6. Fixed failure of IOBP Check.