

IPMI Firmware / BIOS Release Notes Form

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Product Name	X10DSC+
Release Version	3.1c SPS: 3.1.3.72
Release Date	4/27/2019
Previous Version	3.1
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated Intel Server Platform Services 3.1.3.72 for Grantley Refresh Platforms.2. Updated SATA/sSATA RAID OPRM/EFI driver to VROC PreOS v6.1.0.1017.3. Updated VBIOS and VGA EFI Driver to 1.09.4. Updated Haswell-EP/Broadwell-EP CPU microcode from SRV_P_273.5. Updated valid range of IPMI setup item VLAN ID to 1-4094.6. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.7. Forced a global reset if SPI descriptor is not write-protected after BIOS flash.8. Implemented anti-rollback for FDT Read-Only.9. Implemented multi-line IPMI page text and string.

	<p>10. Added support for setting IPv6 Static Router1 prefix length and value in BIOS setup menu feature.</p> <p>11. Updated SATA/sSATA RAID OPRM/EFI driver to VROC PreOS v6.1.0.1017.</p>
New features	N/A
Fixes	<p>1. Displayed the driver health support pages to support LSI (Broadcom 9440-8i) driver health status.</p> <p>2. Fixed malfunction of Windows Consistent Device Naming (CDN).</p> <p>3. Fixed problem of BIOS always retrying boot when Re-Try Boot is Disabled.</p> <p>4. Fixed malfunction of METW if many error events are triggered within a very short time.</p> <p>5. Corrected UUID when LAN is X710.</p>

Release Notes from Previous Release(s)

3.1 (7/24/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS version to 3.1.
3. Added support for IPMI IPV6.
4. Removed unsupported memory frequency options from setup menu.
5. Updated Broadwell-EP RC 4.4.0 release.
6. Modified auto switch bifurcation on CPU slot2 when two AOC-SLG3-2M2s are installed.
7. Fixed problem of Afu /O command clearing SMC SMBIOS region (\$SMC).
8. Fixed problem of SUM OOB GetSataInfo always showing "Configuration Type" as "AHCI" when setting "Configure SATA as" to "RAID" or "IDE".
9. Set Descriptor Region of BIOS Region Write Access to "No".

3.0a (2/8/2018)

1. Implemented enhancement to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
2. Updated Broadwell-EP RC 4.3.0 PLR11 release.
3. Updated BIOS ACM 3.1.1 PW and SINIT ACM 3.1.1 PW.
4. Corrected POST diagnostic signOn string.
5. Added tCCD_L Relaxation item under Memory Configuration menu.
6. Added SumBbsSupportFlag into DAT file.
7. Changed Memory correctable threshold to 100 and enabled Cloaking for Broadwell CPU.
8. Added support for JEDEC NVDIMM.
9. Added Ramaxel JEDEC ID to support Ramaxel memory.
10. Fixed problem of NVDIMM setup items appearing when optimized defaults load even without NVDIMM being installed.
11. Fixed problem of SMBIOS Type4 CPU2 current speed not showing as zero when CPU2 is not installed.
12. Fixed problem of system resetting or hanging after Watch Dog function is enabled during BIOS update.
13. Fixed problem of MWAIT being disabled on BSP only.
14. Fixed inability to get correct Memory CECC DIMM location via SD5.
15. Fixed problem of single channel reading mux when BIOS reads BPN ID.
16. Added workaround to skip retimer for AOC-SLG3-4E4R.
17. Fixed inability to enter setup menu by pressing "DEL" key if Re-try Boot feature is enabled and there are no boot devices.