

IPMI Firmware / BIOS Release Notes Form

Supermicro disclaims all express and implied warranties, including without limitation, the implied warranties of merchantability, fitness for a particular purpose, and non-infringement, as well as any warranty arising from course of performance, course of dealing, or usage in trade. All products, computer systems, dates, and figures specified are preliminary based on current expectations, and are subject to change without notice. Supermicro and the Supermicro logo are trademarks of Super Micro Computer, Inc. in the U.S. and/or other countries. Copyright © 2018 Super Micro Computer, Inc. All rights reserved.

Product Name	X11DPX-T
Release Version	3.2
Release Date	10/18/2019
Previous Version	3.1
Update Category	Recommended
Dependencies	None
Important Notes	None
Enhancements	<ol style="list-style-type: none">1. Updated AMI label 5.14_PurleyCrb_0ACLA049_BETA for BKC WW36 IPU 2019.2 and AMI security update SA50072.2. Updated SINIT/BIOS ACM from BKC WW36 IPU 2019.2 to address CVE-2019-0151 and CVE-2019-0152.3. Updated SPS_E5_04.01.04.339.0 from BKC WW36 IPU 2019.2 to address PSIRT-TA-201905-011.4. Updated Cascade Lake-SP CPU microcode and Skylake-SP microcode.5. Displayed Setup item "ARI Support".6. Added setup item to control 16Gb based Single Die Package DIMM tRFC optimization.7. Disabled ADDDC/SDDC and set PPR as hPPR.
New features	<ol style="list-style-type: none">1. Added Enhanced PPR function and set disabled as default.

Fixes	<ol style="list-style-type: none"><li data-bbox="493 155 1403 239">1. Removed PXE option for "OnBoard LanX Option ROM" when "Boot mode select" is UEFI.<li data-bbox="493 260 1094 294">2. Corrected display of the IPMI AUX revision.
--------------	---

Release Notes from Previous Release(s)

3.1 (5/21/2019)

1. Updated Skylake-SP/Cascade Lake-SP CPU microcode.
2. Updated Intel BKCWW16 2019 PV PLR1.
3. Updated SPS_E5_04.01.04.296.0 from BKC WW16 2019.
4. Updated EIP467272 for AMI SA50069, SA50070.
5. Set SDDC Plus One or SDDC to disabled by default.
6. Updated SATA/sSATA RAID OPROM/EFI driver to VROC PreOS v6.1.0.1017.
7. Set Leaky Bucket that can decrease one memory correctable error count within 2.15 minutes with threshold 512.
8. Set ADDDC to enabled by default.
9. Fixed problem of system halting or rebooting at POST code 0xB2 or 0x92 when total GPT partition number is more than 256 and RSD is enabled.
10. Fixed inability to change IPv6 address or IPv6 Router1 IP address.

3.0c (3/27/2019)

1. Added support for Purley Refresh platform.
2. Enhanced BIOS setup menu to auto-switch the Option ROM's value and hide the unexpected value when boot mode changes.
3. Updated to SPS 4.0.04.381 or above for INTEL-SA-00213 Security Advisory.
4. Temporarily fixed error for SKX (CLX) 4114 CPU memory training with Micron (18ASF2G72PDZ-2G6E1) RDIMM.
5. Fixed failure to boot into VMware OS when set to Maximum Performance even if Monitor/MWAIT is enabled.

3.0b (3/4/2019)

1. Changed BIOS version to 3.0b.
2. Added support for Purley Refresh platform.
3. Updated CPU microcode MB750654_0200005A for Skylake-SP H0/M0/U0 CPUs.
4. Updated SATA RAID OPROM/EFI driver to RSTe PreOS v6.0.0.1024.
5. Added support for Monitor Mwait feature.
6. Added support for RFC4122 UUID format feature to set RFC4122 encoding from build time produced by IPMICFG 1.29 tool or newer.
7. Set PCIe correctable and uncorrectable (non-fatal) error report to disabled by default.
8. Disabled unused SPS ME "CUPS IIO" and "Volumetric Airflow" sensor.
9. Added support for Linux built-in utility efibootmgr.
10. Updated valid range of IPMI setup item VLAN ID to 1-4094.
11. Set NVDIMM ADR timeout to 600us.
12. Enhanced the solution for the error/warning message from dmidecode after a modification by AMIDE.
13. Updated to SPS 4.1.02.174 or above for INTEL-SA-00185 Security Advisory and RC 549.D13 or above for INTEL-SA-00192 Security Advisory.
14. Fixed malfunction of CPU PBF (Prioritized Base Frequency).
15. Fixed incorrect PBF high frequency core number when Hyper-Threading is disabled.
16. Modified UEFI network description to IPv4/IPv6 to follow Industry Standard.
17. Patched problem of system hanging at 0x94 when plugging in Nvidia Tesla T4 card.

2.1 (7/31/2018)

1. Updated CPU microcode to address 'Spectre' derivatives (CVE-2018-3639 & CVE-2018-3640) security issue.
2. Changed BIOS revision to 2.1.
3. Added new setup items "SLOT1 x8 & SLOT2 x8" and "SLOT5 x8 & SLOT6 x8" in IOU0.
4. Updated Purley RC 154.R13, SPS 4.0.04.340 and ACM 1.3.7, SINIT ACM 1.3.4.
5. Updated SATA RAID OPRM/EFI driver to RSTe PreOS v5.4.0.1039.
6. Added support for UEFI mode PXE boot of F12 hot key Net boot.
7. Added BIOS/ME downgrade check for SPS 4.0.4.340 and later versions.
8. Added one event log to record that the event log is full.
9. Corrected help message for TPH BIOS setup items.
10. Displayed PPR setup item.
11. Fixed problem of SUM GetSataInfo showing incorrect "Configuration Type" when setting "Configure sSATA as" to "AHCI" or "RAID" on sSATA controller.
12. Fixed problem of Ctrl + Alt + Del causing system to hang after flashing BIOS.
13. Fixed problem of system hanging when installing Linux OS after COM1 & COM2 IO/IRQ exchange.
14. Fixed failure of WDT function.
15. Added workaround for low GPU P2P bandwidth.
16. Changed Slot Data Bus Width for slot 2 and slot 6.

2.0c (3/13/2018)

1. Updated CPU microcode SRV_P_229 for Skylake-SP H0/M0/U0 stepping CPUs to address 'Spectre' variant 2 (CVE 2017-5715) security issue.
2. Changed BIOS revision to 2.0c.
3. Displayed setup item "ACS Control" to Enable/Disable PCIe Access Control Services Extended Capability, with Enabled as default.
4. Added support for VMD settings to be preserved after flashing, with disabled as default.
5. Changed maximum speed in SMBIOS type 4 to 4500Mhz.
6. Changed RC default setting to upgrade POST Memory correctable error to uncorrectable error and map out the channel.
7. Fixed problem of DMI being cleared when running SUM LoadDefaultBiosCfg.
8. Disabled CPU2 IIO PCIe root port ACPI hot plug function.
9. Fixed issue with IPMI force boot.
10. Fixed inability to set memory policy.
11. Fixed inability to reset the system under DOS by pressing "Ctrl-Alt-Del" on USB keyboard when "Port 60/64 Emulation" is disabled.
12. Fixed problem of SMC skipping first boot option in UEFI mode when retrying boot.
13. Fixed inability of some CPUs to display correct microcode revision under BIOS setup menu.
14. Patched a workaround for some motherboards' on board LAN having correctable error or GEN speed drop.

2.0b (1/31/2018)

1. Updated BIOS version to 2.0b and changed BIOS size to 32MB.
2. Rolled back CPU microcode to MB750654_02000035 for Skylake-EP H0/M0/U0 stepping CPUs.
3. Fixed failure of BIOS ECO ATT test case 306.
4. Fixed problem of PCIe slot 7 strings name in BIOS setup not matching motherboard's silkscreen.
5. Fixed failure to check IOBP settings.